

Micronas.7397

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Czech et al. **GROUP:** 2826
SERIAL NO: 10/761,144 **EXAMINER:** Tan N. Tran
FILED: January 20, 2004
FOR: VOLTAGE COUPLING CIRCUIT FOR AN INTEGRATED CIRCUIT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:


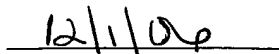
AMENDMENT AFTER ALLOWANCE PER 37 C.F.R. §1.312

Entry of this amendment is requested in order to correct some grammatical and typographical errors in the specification. No new matter is added to this application by these amendments.

Please amend the application as follows:

IN THE SPECIFICATION:

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Kim M. Hume

Date